NPN Triple Diffused Planar Silicon Transistor



2SD2688LS

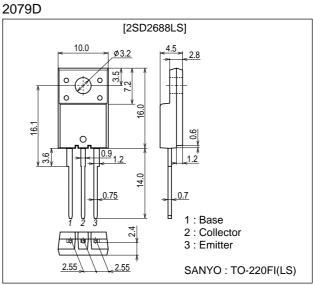
Color TV Horizontal Deflection Output Applications

Features

- High speed.
- High breakdown voltage(VCBO=1500V).
- High reliability(Adoption of HVP process).
- Adoption of MBIT process.
- On-chip damper diode.

Package Dimensions

unit : mm



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		10	А
Collector Current (Pulse)	ICP		25	A
Collector Dissipation	De		2.0	W
	PC	Tc=25°C	35	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =800V, I _E =0			10	μΑ
Collector Cutoff Current	ICES	V _{CE} =1500V, R _{BE} =0			1.0	mA
Collector Sustain Voltage	VCEO(sus)	IC=100mA, IB=0	800			V
Emitter Cutoff Current	IEBO	V _{BE} =4V, I _C =0	40		130	mA

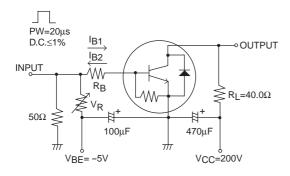
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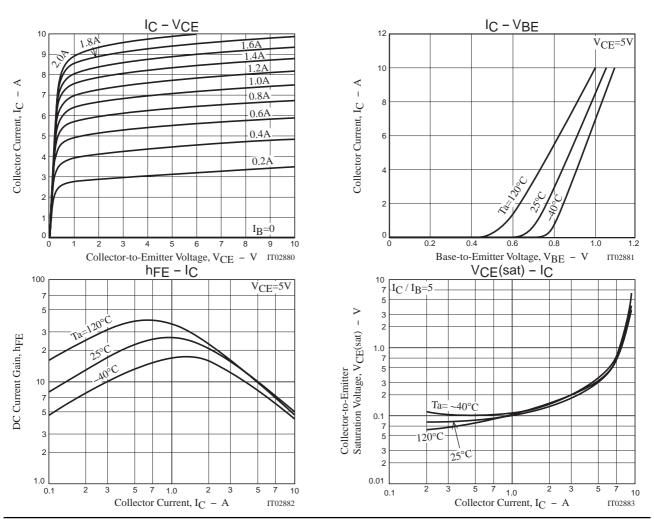
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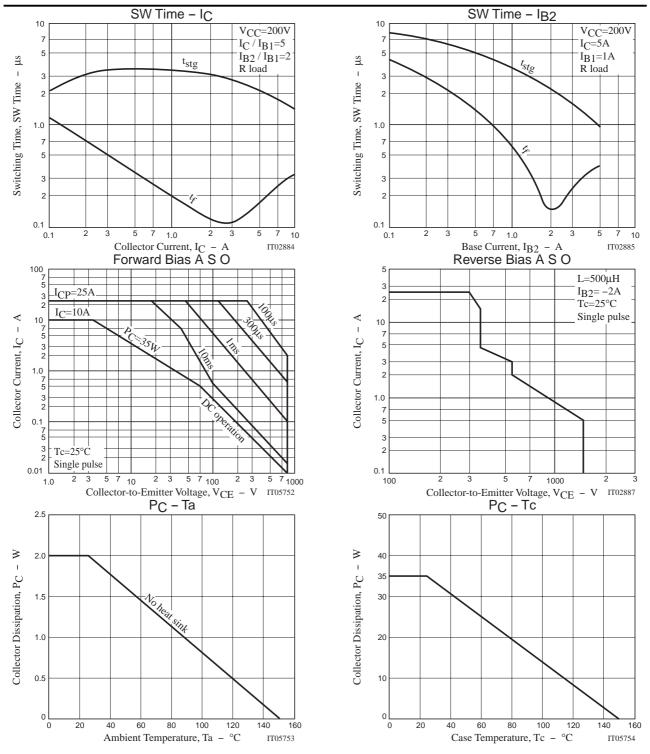
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	IC=7.2A, IB=1.44A			3	V
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =7.2A, I _B =1.44A			1.5	V
DC Current Gain	hFE1	VCE=5V, IC=1A	15			
	hFE2	V _{CE} =5V, I _C =8A	5		8	
Diode Forward Voltage	VF	IEC=8A			2	V
Fall Time	tf	IC=5A, IB1=1A, IB2=-2A			0.3	μs

Switching Time Test Circuit





No.7526-2/4



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